

**AMENDMENTS TO THE TITLE**

**Kindly replace the title used in the International Application with the following:**

ELECTRONIC APPARATUS AND CONTROL METHOD THEREOF USING  
SAMPLE RATE CONVERTER

## **AMENDMENTS TO THE SPECIFICATION**

**Please amend the paragraph beginning on page 12, line 23, as follows:**

In the first preferred embodiment of the present invention, the host apparatus 160 includes an insertion slot (not shown) to which the functional module card 110 is attached. A connector provided to the insertion slot of the host apparatus 160 is connected to a connector (not shown) of the functional module card 110 that is attached to the ~~card~~insertion slot. Via the connectors, the host apparatus 160 outputs electric power, control commands, data, and the like to the functional module card 110, and receives responses, video and audio signals of moving pictures, data, and the like outputted from the functional module card 110. In the first preferred embodiment, the host apparatus 160 is a PDA (Personal Digital Assistants). The functional module card 110 is inserted into the insertion slot for the card, and the host apparatus 160 outputs video data and audio data of terrestrial digital broadcasting received by the functional module card 110 to a display 180 and a loudspeaker 181. The display 180 and loudspeaker 181 may be built in the host apparatus 160.

**Please amend the paragraph beginning on page 13, line 14, as follows:**

Alternatively, the host apparatus 160 may be the other apparatus (such as a notebook type personal computer or a mobile telephone). The host apparatus 160 enables any one of not only the functional module card 110 but also various kinds of other functional module cards (such as a functional module card including only a memory function and a functional module card including the memory function and the other function (e.g., a radio communication function)) to be attached to the ~~card~~insertion slot (including the connector provided to the ~~card~~insertion slot) and to operate.

**Please amend the paragraph beginning on page 15, line 9, as follows:**

To the host apparatus 160, the functional module card 110 transmits a response to a command from the host apparatus 160 via the command/response line 142. The functional module card 110 transmits data to the host apparatus 160 via the command/response line 142 or the data lines 143 to 146. Via one of the data lines 143 to 146, the functional module card 110 transmits an interrupt signal to the host apparatus 160 within a time period in which no data is transmitted. For example, the functional module card 110 transmits compressed data of a received digital signal stream, the interrupt signal and data stored in the flash memory 119 to the host apparatus 160. The above-mentioned ~~commands~~ interrupt signal, responses and data are outputted in synchronization with the transmission clock transmitted from the host apparatus 160 via the clock line 141.

**Please amend the paragraph beginning on page 28, line 19, as follows:**

In step 228, the card controller 116 transmits the interrupt signal to the host controller 161 via one of the data lines 143 to 146. The host controller 161 receives the interrupt signal (step 203). The host controller 161 transmits an interrupt cause information transmission request command to the communication section 117 via the command/response line 142 (step 204). The communication section 117 receives the interrupt cause information transmission request command (step 229). To the host controller 161, via the command/response line 142, the communication section 117 transmits information indicating that the cause of the interruption is based on the fact that the volume of the compressed video and audio data newly stored in the buffer memory 115 has reached the predetermined volume (step 230). The host controller 161 receives interrupt cause information (step 205). The host ~~controller~~ controller 161 transmits a notice indicating the reception of the interrupt signal to the data rate detector 175.

**Please amend the paragraph beginning on page 34, line 15, as follows:**

The buffer controller 414 stores the video data and audio data in the buffer memory 415. The buffer controller 414 memorizes a stored data volume of the video data and audio data stored in the buffer memory 415. Each time when data volume of the video data and audio data newly stored to the buffer memory 415 reaches a predetermined value "R" ("R" is an arbitrary positive integer), the buffer controller 415 transmits information that the data volume of the video and audio data newly stored to the buffer memory 415 has reached the predetermined value, to the card controller 116. Upon receiving the information, the card controller 116 transmits an interrupt signal (an interrupt signal requesting the host apparatus 460 to read out the video data and audio data) to the host apparatus 460. Upon receiving the interrupt signal, the host apparatus 460 transmits a video data and audio data reading out request command to the card controller 116. The buffer controller 414 reads out the video data audio data from the buffer memory 415 in response to the video data and audio data reading out request command, and transmits read-out video data and audio data to the host apparatus 460 via the card controller 116.

**Please amend the paragraph beginning on page 35, line 17, as follows:**

The counter 476 of the data rate detector 475 memorizes rate difference information, which is a difference between a data rate of the video signal and audio signal (the broadcasting signal) received by the functional module card 410 and a data rate of the video signal and audio signal outputted from the host apparatus 460. Upon receiving the notice indicating the reception of the interrupt signal from the host controller 161, the data rate detector 475 ~~subtracts one from~~ adds one to the rate difference information. Upon receiving a notice indicating output of the video data and audio data from the buffer controller 166, the data rate detector 475 subtracts one from the rate difference information. Each time when data volume of outputted video data and audio data reaches a predetermined value "R" ("R" denotes a frequency division ratio at which

the functional module card 410 frequency-divides an amount of information of the video data and audio data and outputs the interrupt signal), an output information is transmitted to the data rate detector ~~175~~ 475. Upon receiving the output information from the buffer controller 166, the data rate detector ~~175~~ 475 adds one to the rate difference information.

**Please amend the paragraph beginning on page 38, line 1, as follows:**

Steps 532, 533 and 507 will be described. In step 532, the card controller 116 reads out the video data and audio data from the buffer memory ~~115~~ 415 via the buffer controller 414. In step 533, the communication section 117 transmits the video data and audio data to the host controller 161 via at least one line (preferably four lines) of the data lines 143 to 146. The host controller 161 receives the video data and audio data (step 507), and transmits the video data and audio data to the sample rate converter 165.